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PATENT & TRADEMARK OFFICE

STEREOLITHOGRAPHIC METHODS FOR FABRICATING
HERMETIC SEMICONDUCTOR DEVICE PACKAGES AND
SEMICONDUCTOR DEVICES INCLUDING STEREO-
LITHOGRAPHICALLY FABRICATED HERMETIC PACKAGES

Inventor: Warren M. Farnworth

Serial No.: 10/074,840

Docket No.: 4212.3US

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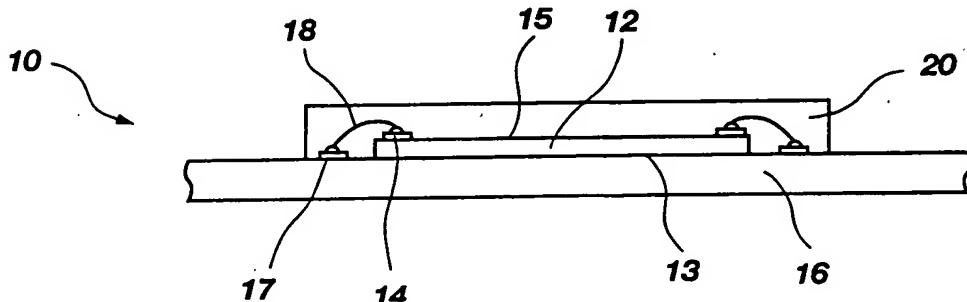


Fig. 1

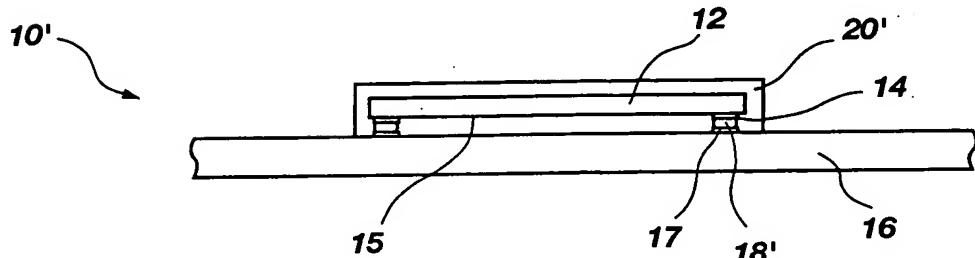


Fig. 2

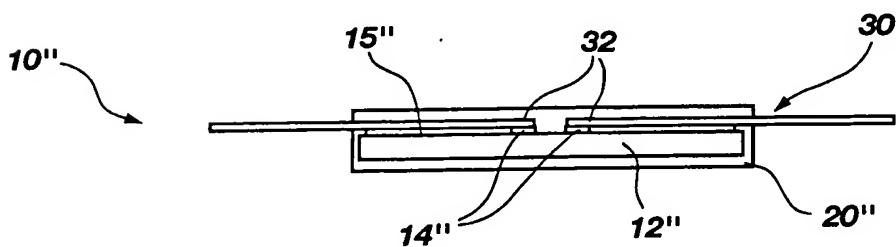


Fig. 3

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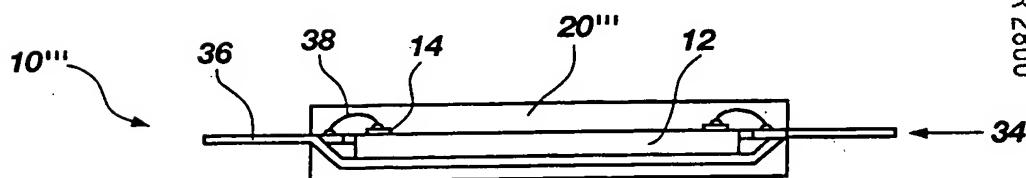


Fig. 4



STEREOLITHOGRAPHIC METHODS FOR FABRICATING
HERMETIC SEMICONDUCTOR DEVICE PACKAGES AND
MICRODEVICES INCLUDING STEREO-
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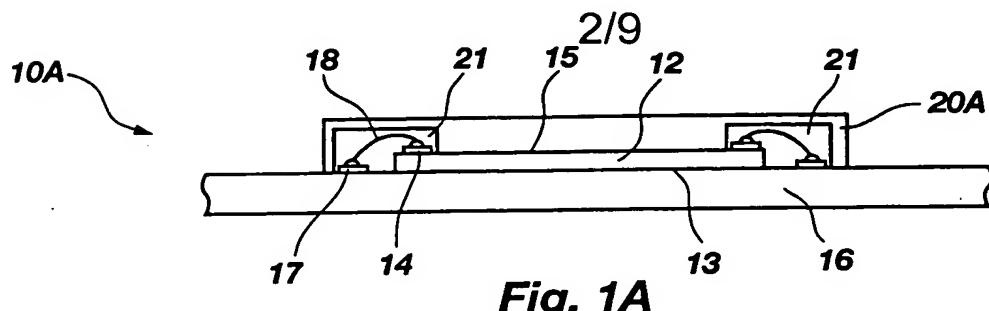


Fig. 1A

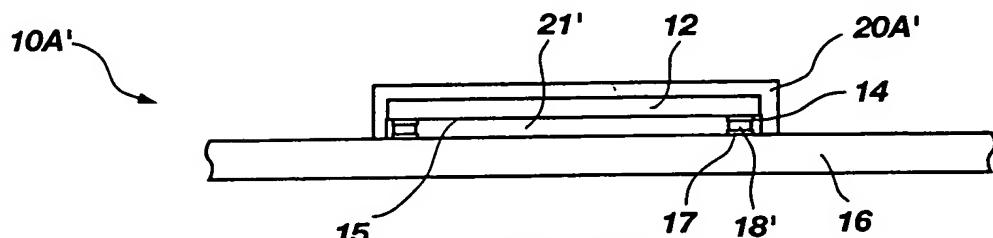


Fig. 2A

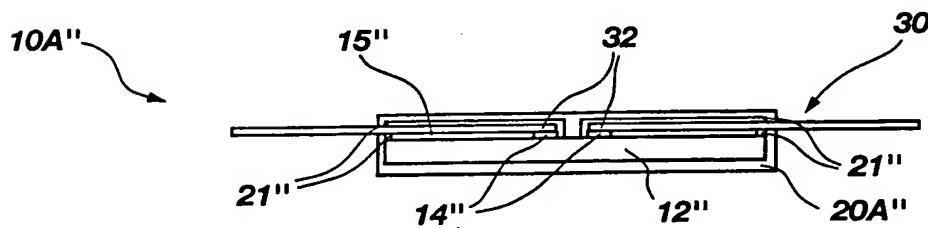


Fig. 3A

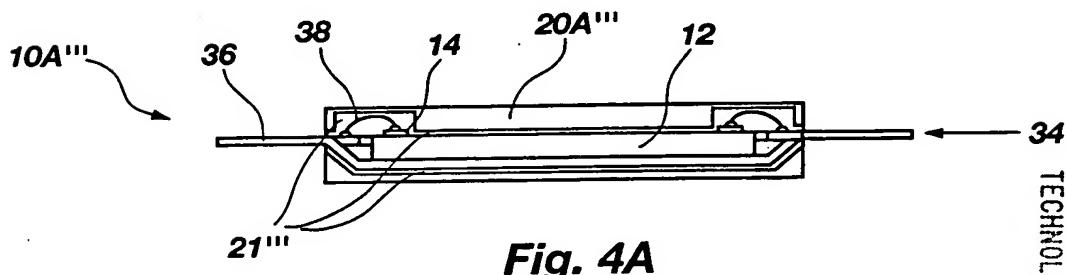


Fig. 4A

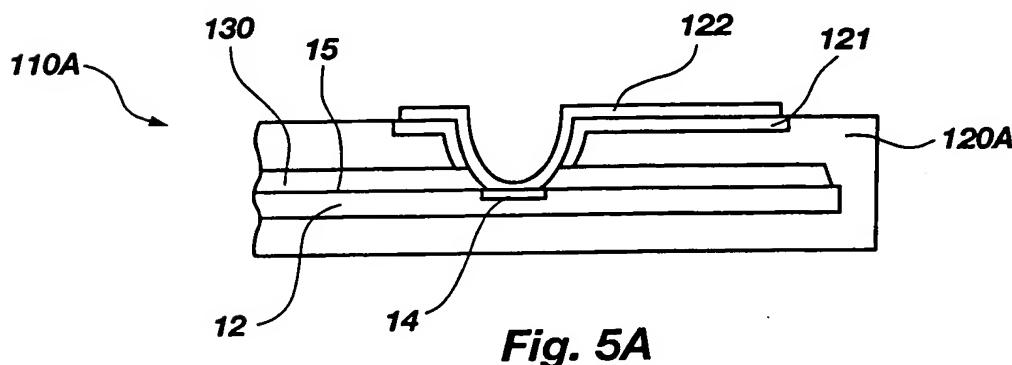


Fig. 5A

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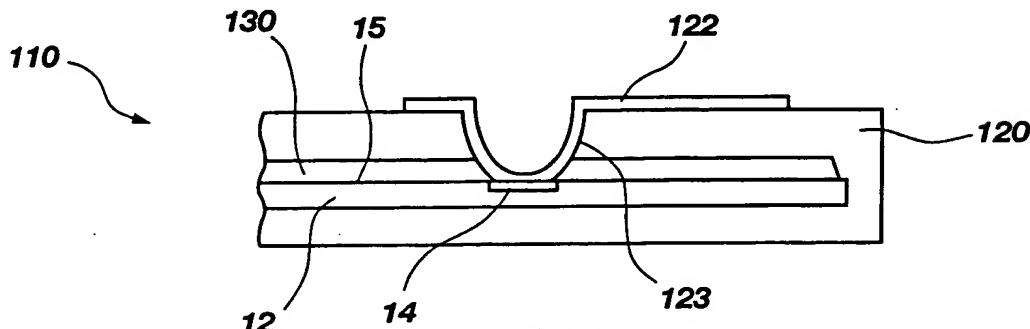


Fig. 5

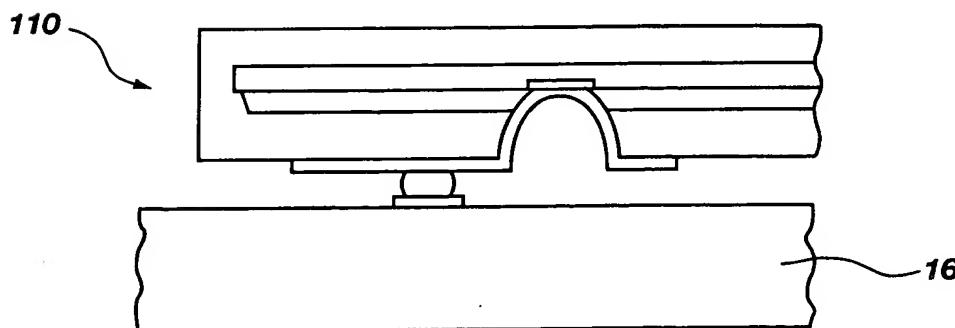


Fig. 6

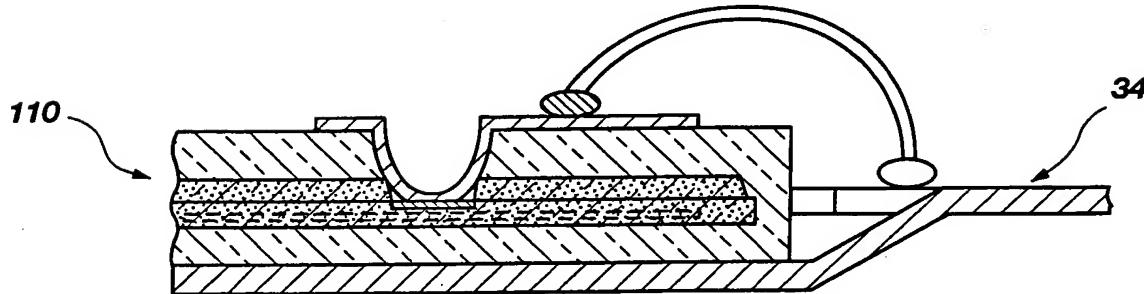


Fig. 7



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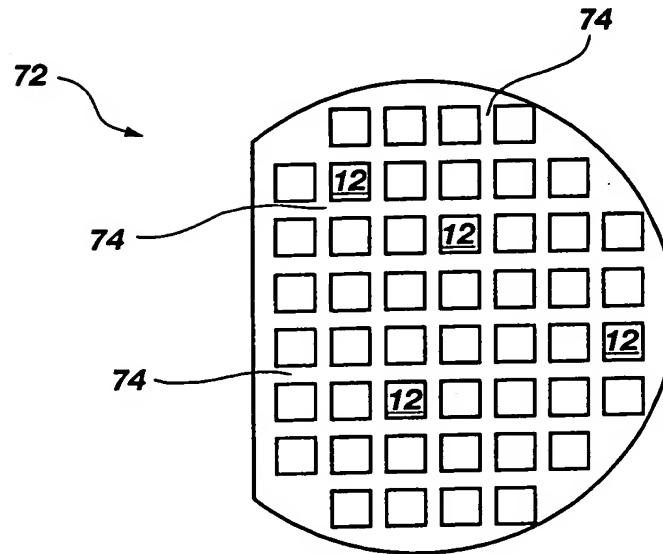


Fig. 8

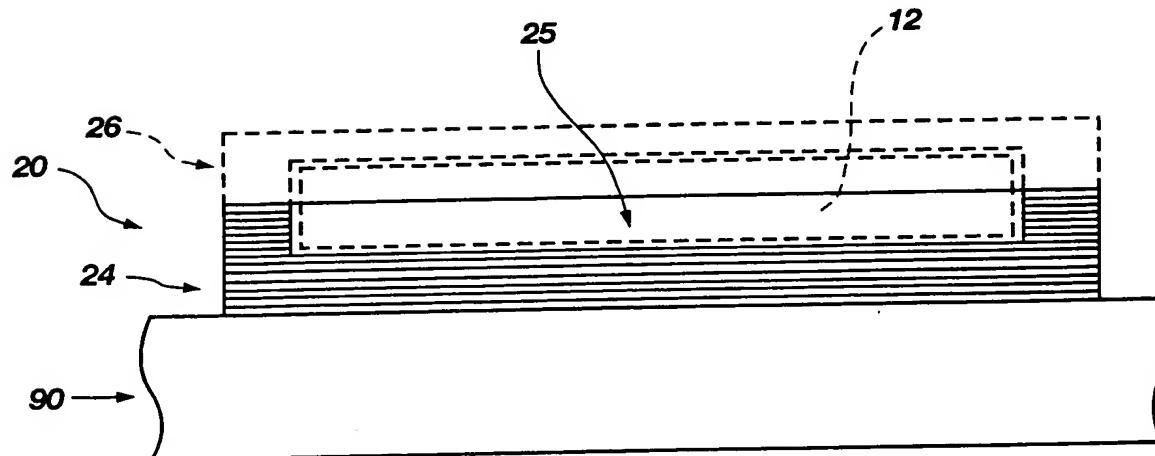


Fig. 18

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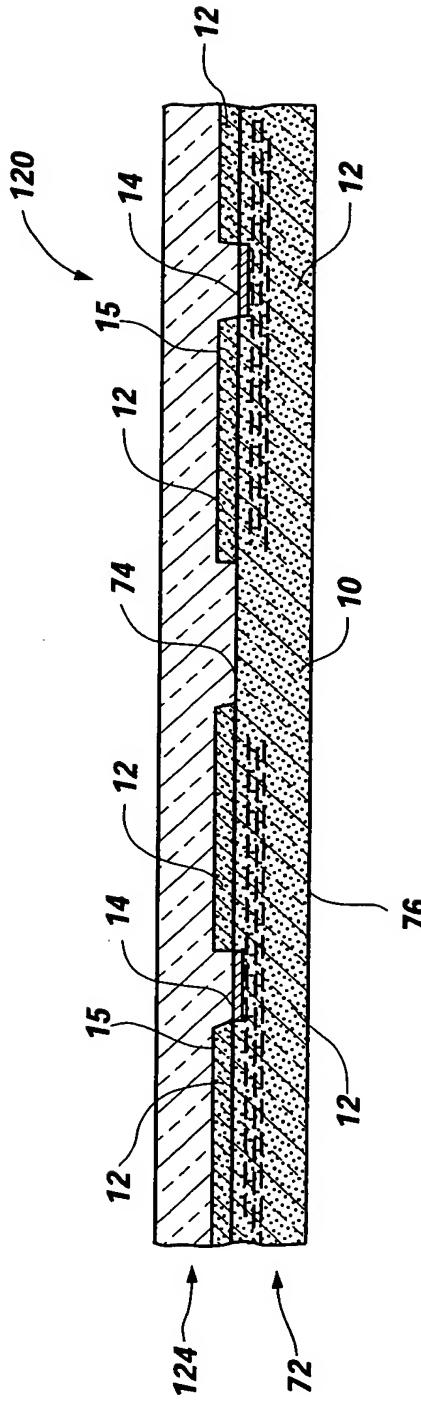


Fig. 9

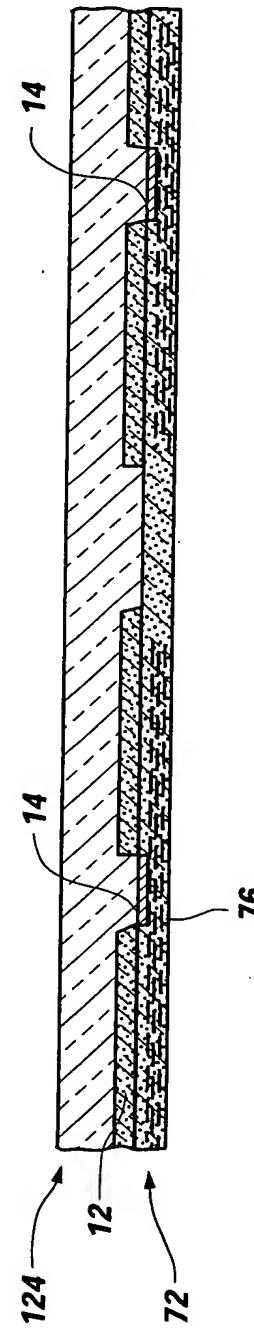


Fig. 10



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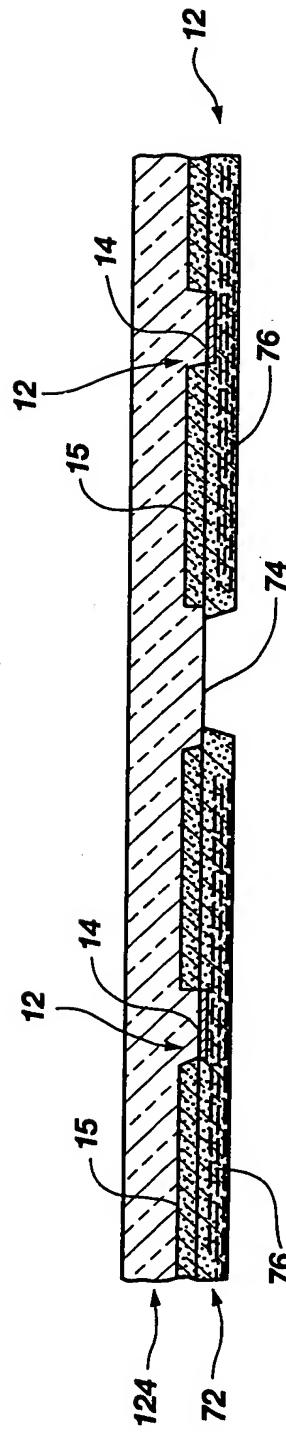


Fig. 11

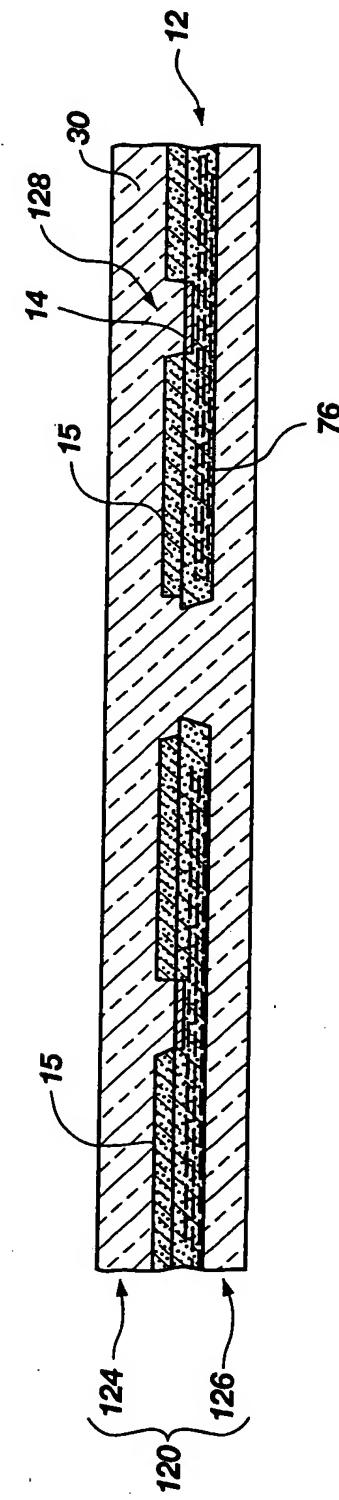


Fig. 12



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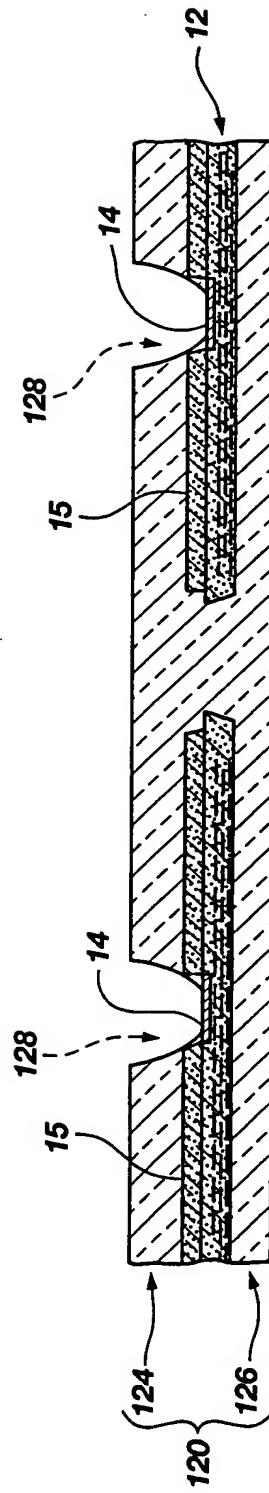


Fig. 13

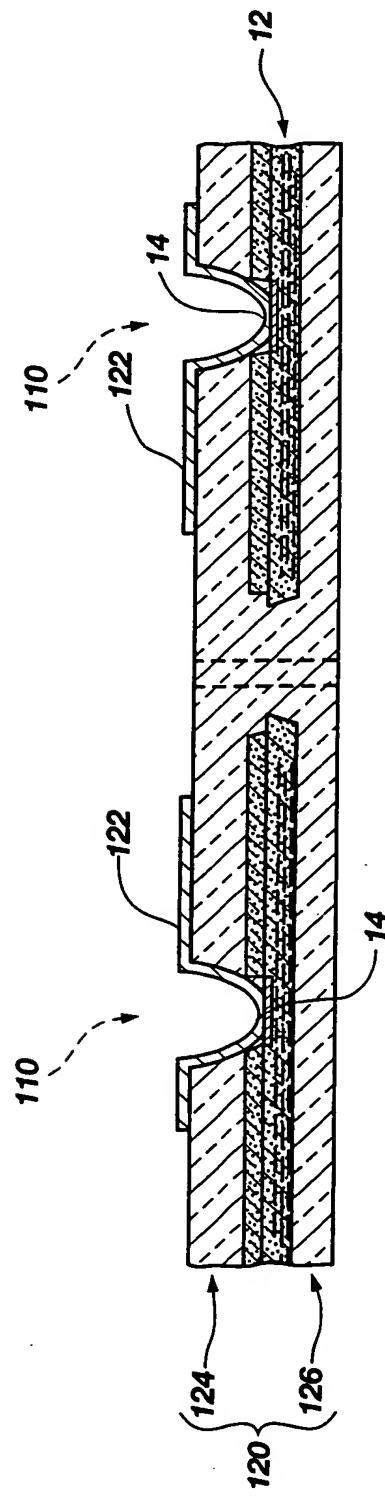


Fig. 14

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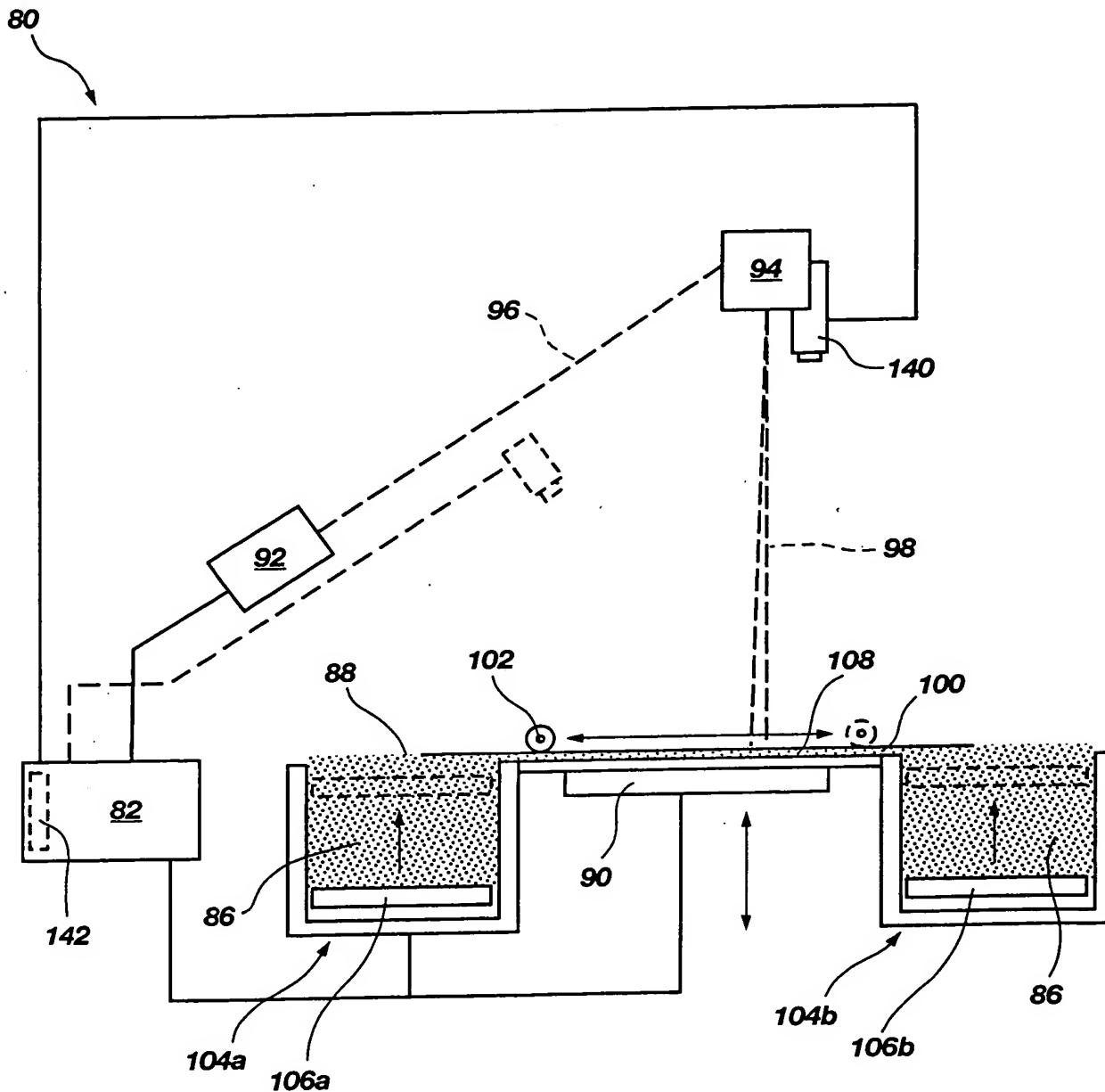


Fig. 15



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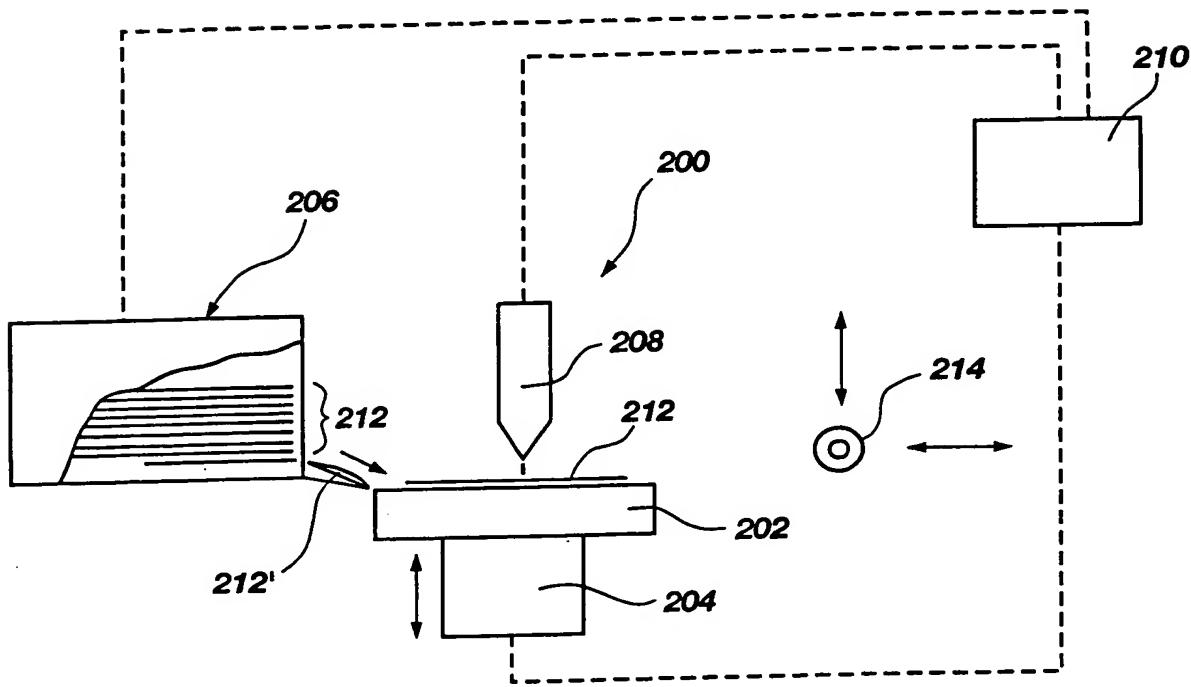


Fig. 16

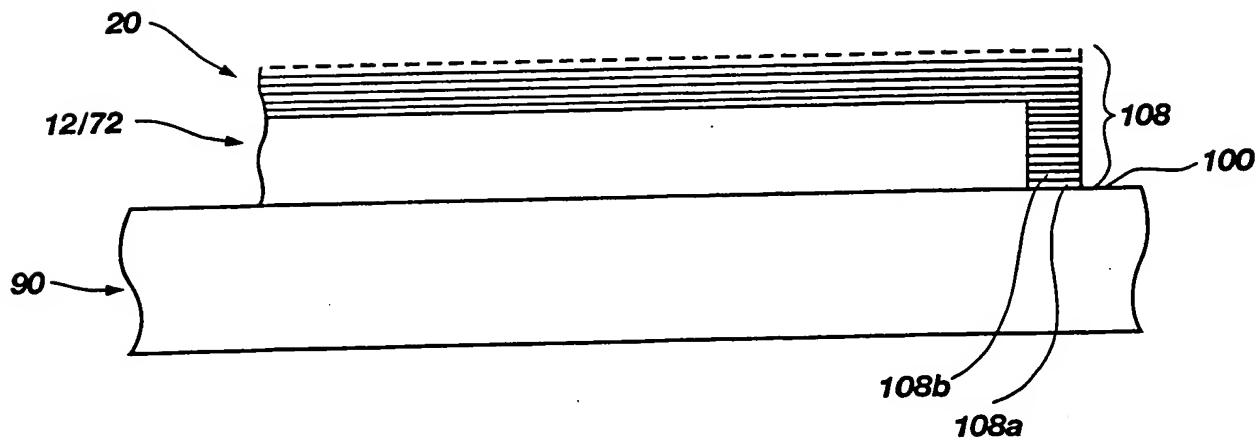


Fig. 17